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Bib Data Sheet

CONFIRMATION NO. 4970

SERIAL NUMBER 10/036,429	FILING DATE 01/07/2002 RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 108287-00007
APPLICANTS Mitsuru Sasaki, Kawasaki, JAPAN;				
** CONTINUING DATA <i>W</i> THIS APPLICATION IS A DIV OF 09/726,607 12/01/2000 USPATENT 6,720,797 04/13/2004				
** FOREIGN APPLICATIONS <i>W</i> JAPAN 2000-180393 06/15/2000				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/01/2002				
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged <i>W</i> Examiner's Signature <i>W</i> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 23	TOTAL CLAIMS 9
ADDRESS ARENT FOX KINTNER PLOTKIN & KAHN, PLLC 1050 CONNECTICUT AVENUE, N.W., SUITE 400 WASHINGTON, DC 20036-5339				
TITLE Path transistor circuit and method for designing the same, a device and method for optimizing logic circuits, and a computer product				
FILING FEE RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <input type="checkbox"/> Credit	